ImageWriter

Microchip PIC16F648A Device Support Detail & Setup

ImageWriter Support Hierarchy

Manufacturer: Microchip

Device: PIC16F648AHV

Objects for PIC16F648AHV

Flow Object Name:	*Program / Execute*	*Verify*	*Load*	*Object Size (Words)*
FLASH	●	●	●	0x1000
EEPROM	●	●	●	0x100[1]
USER ID	●	●	●	0x04
CONFIG	●	●	●	0x01
DEVICE ID			●	0x01
ERASE FLASH	●			
ERASE EEPROM	●			
ERASE ALL	●			
p_MCT16F[2]	●		●	0x08

[1] EEPROM Data is LSB byte data stored in 16-bit words. MSB is read as device dependent don't cares (usually 0x3F or 0x00). Verify operations use entire 16-bit word.
[2] Default Clock Speed = 500KHz

Physical Layer Connector Setup for PIC16F648A
High Voltage Method

ImageWriter:		Target:
Connector Pin #	***Pin Name***	***Connections***
1	GND	GND
2	GND	GND
3	TXD (MOSI/TDO)	See note 1
4	RXD (MISO/TDI)	RB7
5	RTS (TMS)	RB4
6	CTS	NOCONN
7	XCLK	RB6
8	RST	NOCONN
9	SCL	NOCONN
10	SDA	NOCONN
11	GND	GND
12	GND	GND
13	VPP/EXT_IN	12.0V DC
14	VPP/EXT_OUT	MCLR
15*	Target V+ (7.0V max)	Supply 4.5-5.5 V
16*	Target V+ (7.0V max)	Supply 4.5-5.5 V
17	GND	GND
18	GND	GND
19	ImageWriter Power	Supply 9-24 VDC
20	ImageWriter Power	Supply 9-24 VDC

1 Pin 3 must be connect to Pin 4 with an external 1K Ohm resistor in series.
*Maximum Pin-Driver output voltage = 4.2V.
*Microchip specifies that VCC may be 2.0-5.0V for programming operations; however bulk-erase is only valid with VCC at 4.5V or higher. If you use erase, you must provide 4.5V DC or higher VCC levels.

Support Notes:

1. Onboard programming is supported via High Voltage method only. The LVP config bit will not affect device programming.
2. Data verification does not occur automatically during program. Include a Verify operation in your flow to ensure data integrity (highly recommended).

3. Config bit mapping is per manufacturer data-sheet. All bits stored in ImageWriter memory must be properly set.
4. "ERASE ALL" object will erase code-protected devices by resetting the Config array. FLASH, EEPROM, and USER ID arrays are also erased during this process.

Disclaimer

Typical implementation of **ImageWriter** includes but is not limited to external power provision to **ImageWriter**, device access restrictions on the target board, external hardware on the target board, or software changes created by the user affecting timing, device erasure, programming, or verification. Data I/O warranty coverage applies only when the product is used in conformance with technical specifications put forth by Data I/O and by the semiconductor device manufacturer.

In the interest of achieving the highest levels of safety and quality, Data I/O recommends that all programming processes be tested and documented in a prototype project prior to transition to volume manufacturing. For additional information see In-System Programming Design Guidelines (http://www.dataio.com/isp).

ImageWriter programs semiconductor devices through the use of circuits designed precisely to operate in accordance with specifications outlined by the device manufacturer. Provision and calibration of power supplies are critical for proper operation. **ImageWriter** has been fully tested by Data I/O Corporation for quality performance.